

**R6004END** 

V <sub>DSS</sub>	600V
R <sub>DS(on)</sub> (Max.)	0.98Ω
I <sub>D</sub>	±4.0A
PD	58W

# Features

- 1) Low on-resistance.
- 2) Fast switching speed.
- 3) Gate-source voltage (V<sub>GSS</sub>) guaranteed to be ±20V.
- 4) Drive circuits can be simple.
- 5) Parallel use is easy.

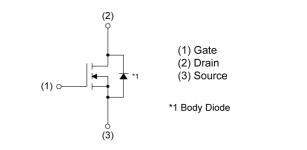
Application

Switching

6) Pb-free lead plating ; RoHS compliant

# • Outline TO-252 SC-63 CPT3 (1) (2) (2) (2) (3)

# ●Inner circuit



# Packaging specifications

	Packing	Embossed Tape
	Reel size (mm)	330
Туре	Tape width (mm)	16
	Basic ordering unit (pcs)	2500
	Taping code	TL
	Marking	R6004E

# • Absolute maximum ratings (T<sub>a</sub> = 25°C ,unless otherwise specified)

Parameter		Symbol	Value	Unit
Drain - Source voltage		V <sub>DSS</sub>	600	V
Continuous dusin suument	T <sub>C</sub> = 25°C	۱ <sub>D</sub> *1	±4.0	А
Continuous drain current	T <sub>C</sub> = 100°C	۱ <sub>D</sub> *1	±2.2	А
Pulsed drain current		I <sub>DP</sub> *2	±8.0	А
Gate - Source voltage	Static	V <sub>GSS</sub>	±20	V
	AC(f>1Hz)		±30	V
Avalanche current, repetitive		$I_{AR}^{*3}$	0.8	А
Avalanche energy, single pulse		$E_{AS}^{*3}$	46	mJ
Avalanche energy, repetitive		E <sub>AR</sub> *3	0.13	mJ
Power dissipation ( $T_C = 25^{\circ}C$ )		P <sub>D</sub> *4	58	W
Junction temperature		Tj	150	°C
Operating junction and storage temperature range		T <sub>stg</sub>	-55~+150	°C

# • Absolute maximum ratings ( $T_a = 25^{\circ}C$ )

Parameter	Symbol	Conditions	Values	Unit
Reverse diode dv/dt	dv/dt	-	15	V/ns
Drain - Source voltage slope	dv/dt	V <sub>DS</sub> = 480V, T <sub>j</sub> = 25°C	50	V/ns

# •Thermal resistance

Parameter	Sumbol	Values			Unit
Falameter	Symbol	Min.	Тур.	Max.	Unit
Thermal resistance, junction - case	R <sub>thJC</sub>	-	-	2.2	°C/W
Thermal resistance, junction - ambient	R <sub>thJA</sub> *5	-	-	147	°C/W
Soldering temperature, wavesoldering for 10s	T <sub>sold</sub>	-	-	265	°C

# •Electrical characteristics (T<sub>a</sub> = 25°C)

Deremeter	Symbol Conditions		Values			Unit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Drain - Source breakdown voltage	V <sub>(BR)DSS</sub>	$V_{(BR)DSS}$ $V_{GS} = 0V, I_D = 1mA$		-	-	V
		V <sub>DS</sub> = 600V, V <sub>GS</sub> = 0V				
Zero gate voltage drain current	I <sub>DSS</sub>	T <sub>j</sub> = 25°C	-	0.1	100	μA
		T <sub>j</sub> = 125°C	-	-	1000	
Gate - Source leakage current	I <sub>GSS</sub>	$V_{GS}$ = ±20V, $V_{DS}$ = 0V	-	-	±100	nA
Gate threshold voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = 10V, I <sub>D</sub> = 1mA	2	-	4	V
		V <sub>GS</sub> = 10V, I <sub>D</sub> = 1.5A				
Static drain - source on - state resistance	R <sub>DS(on)</sub> *6	T <sub>j</sub> = 25°C	-	0.9	0.98	Ω
		T <sub>j</sub> = 125°C	-	1.36	-	
Gate resistance	R <sub>G</sub>	f =1MHz, open drain	-	16.7	-	Ω





# • Electrical characteristics ( $T_a = 25^{\circ}C$ )

Deremeter	C: make al	Conditions		Values		Unit	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Forward Transfer Admittance	Y <sub>fs</sub>  ⁵6	V <sub>DS</sub> = 10V, I <sub>D</sub> = 2A	1.5	3.0	-	S	
Input capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V	-	250	-		
Output capacitance	C <sub>oss</sub>	V <sub>DS</sub> = 25V	-	250	-	pF	
Reverse transfer capacitance	C <sub>rss</sub>	f = 1MHz	-	30	-		
Effective output capacitance, energy related	C <sub>o(er)</sub>	V <sub>GS</sub> = 0V	-	14	-		
Effective output capacitance, time related	C <sub>o(tr)</sub>	V <sub>DS</sub> = 0V to 480V	-	57	-	рF	
Turn - on delay time	t <sub>d(on)</sub> *6	$V_{DD} \simeq 300 \text{V}, \text{V}_{GS} = 10 \text{V}$	-	22	-		
Rise time	t <sub>r</sub> *6	I <sub>D</sub> = 2A	-	22	-		
Turn - off delay time	t <sub>d(off)</sub> *6	R <sub>L</sub> ≃ 150Ω	-	55	-	ns	
Fall time	t <sub>f</sub> *6	R <sub>G</sub> = 10Ω	-	40	-		

# • Gate charge characteristics ( $T_a = 25^{\circ}C$ )

Parameter	Sumbol	Conditions	Values			l loc't
	Symbol Conditions	Conditions	Min.	Тур.	Max.	Unit
Total gate charge	$Q_g^{*6}$	V <sub>DD</sub> ≃ 300V,	-	15	-	
Gate - Source charge	Q <sub>gs</sub> *6	I <sub>D</sub> = 4A,	-	2.5	-	nC
Gate - Drain charge	Q <sub>gd</sub> *6	V <sub>GS</sub> = 10V	-	10	-	
Gate plateau voltage	V <sub>(plateau)</sub>	V <sub>DD</sub> = 300V, I <sub>D</sub> = 4A	-	6.5	-	V

\*1 Limited only by maximum channel temperature allowed.

\*2 Pw  $\leq$  10µs, Duty cycle  $\leq$  1%

\*3 L $\doteqdot$ 100mH, V<sub>DD</sub>=50V, R<sub>G</sub>=25 $\Omega$ , STARTING T<sub>i</sub>=25°C

\*4 T<sub>C</sub>=25°C

\*5 Mounted on a epoxy PCB FR4 (20mm x 20mm x 0.8mm)

\*6 Pulsed

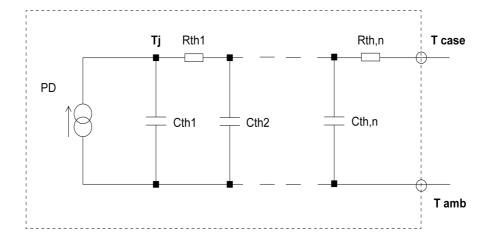


# •Body diode electrical characteristics (Source-Drain) (T<sub>a</sub> = 25°C)

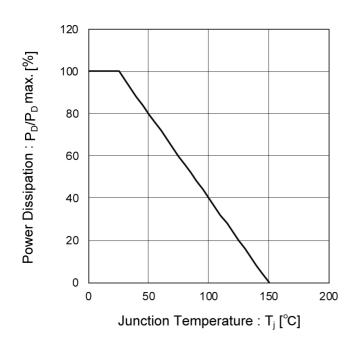
Parameter	Sumbol	Conditions	Values			Unit
	Symbol	Conditions	Min.	Тур.	Max.	Unit
Continuous forward current	ا <sub>S</sub> *1	T <sub>c</sub> = 25°C	-	-	4.0	А
Pulse forward current	۱ <sub>SP</sub> *2	1 <sub>c</sub> - 25 C	-	-	8.0	А
Forward voltage	V <sub>SD</sub> *6	V <sub>GS</sub> = 0V, I <sub>S</sub> = 4A	-	-	1.5	V
Reverse recovery time	t <sub>rr</sub> *6		-	320	-	ns
Reverse recovery charge	Q <sub>rr</sub> *6	I <sub>S</sub> = 4A, V <sub>GS</sub> =0V di/dt = 100A/µs	-	2.4	-	μC
Peak reverse recovery current	I <sub>mm</sub> *6		-	15	-	А

# •Typical Transient Thermal Characteristics

Symbol	Value	Unit	Symbol	Value	Unit
R <sub>th1</sub>	1.3		C <sub>th1</sub>	0.0015	
R <sub>th2</sub>	2.3	K/W	C <sub>th2</sub>	0.0102	Ws/K
R <sub>th3</sub>	21.7		C <sub>th3</sub>	0.127	







# Fig.1 Power Dissipation Derating Curve

# Fig.2 Normalized Transient Thermal Resistance vs. Pulse Width

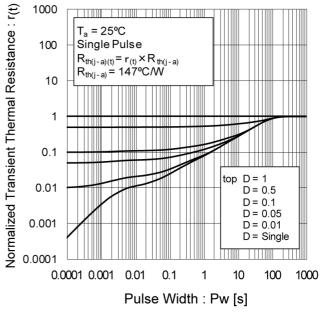
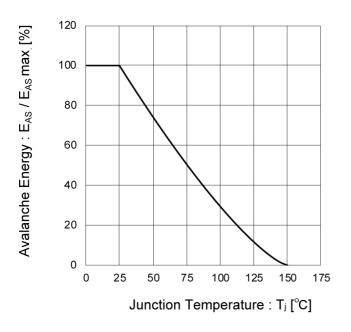


Fig.3 Avalanche Energy Derating Curve vs. Junction Temperature





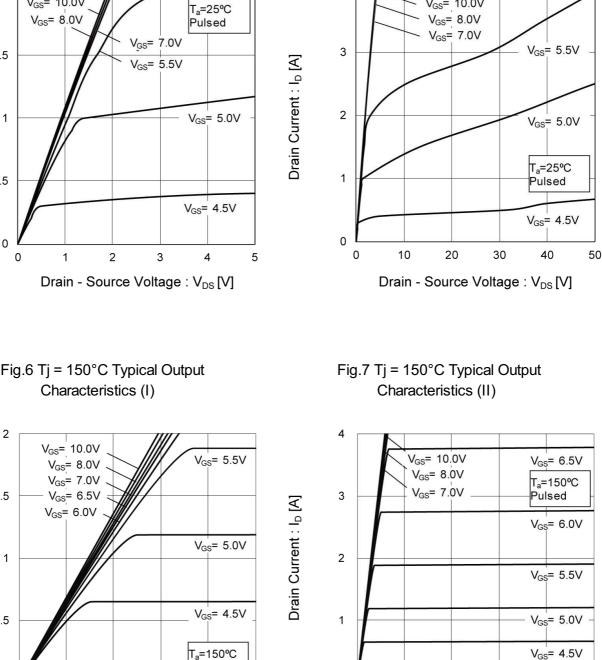


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### Fig.4 Typical Output Characteristics(I)

Fig.5 Typical Output Characteristics(II)

V<sub>GS</sub>= 10.0V



0

0

10

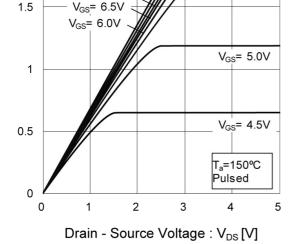
4

30

20

Fig.6 Tj = 150°C Typical Output





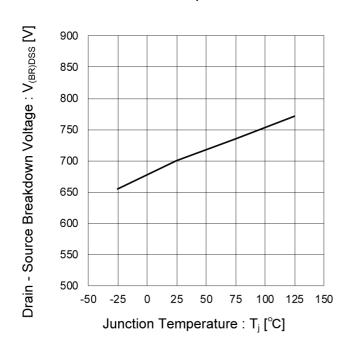
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40

50



## Fig.8 Breakdown Voltage vs. Junction Temperature

Fig.9 Typical Transfer Characteristics

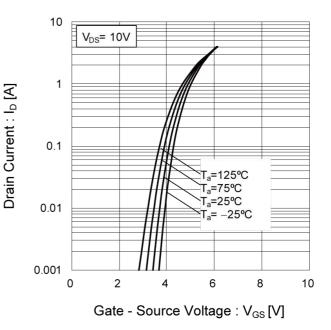
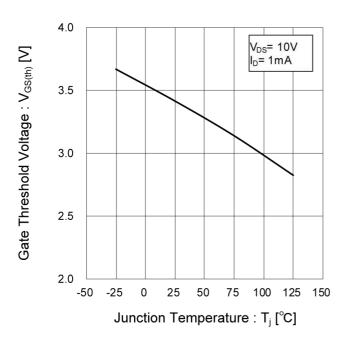
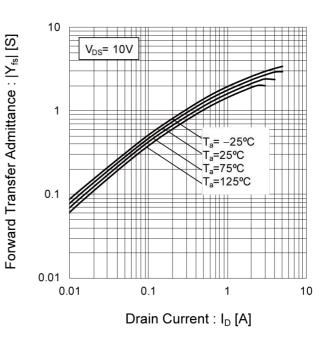


Fig.10 Gate Threshold Voltage vs. Junction Temperature



# Fig.11 Forward Transfer Admittance vs. Drain Current





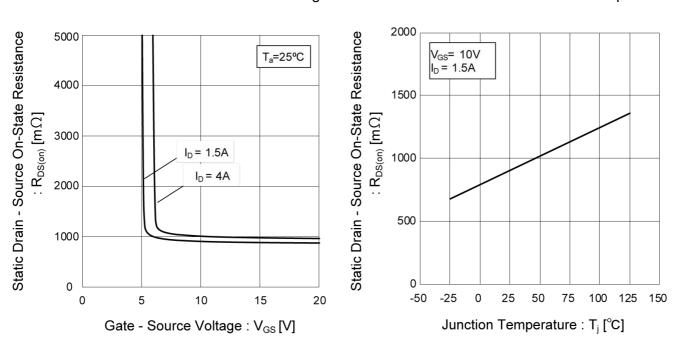


Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage

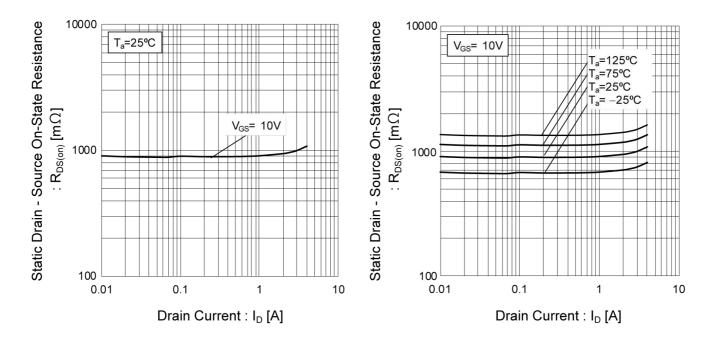
Fig.14 Static Drain - Source On - State

Resistance vs. Drain Current(I)

Fig.15 Static Drain - Source On - State Resistance vs. Drain Current(II)

Fig.13 Static Drain - Source On - State

Resistance vs. Junction Temperature





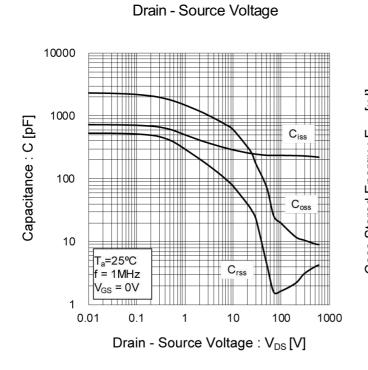


Fig.16 Typical Capacitance vs.

### Fig.17 Coss Stored Energy

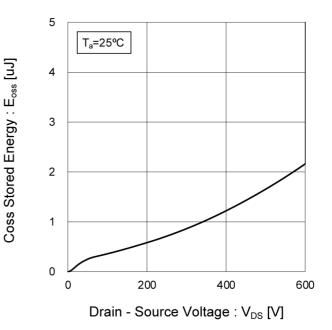
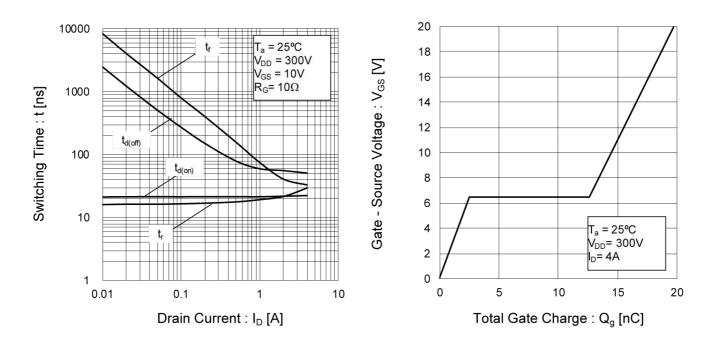


Fig.18 Switching Characteristics

Fig.19 Dynamic Input Characteristics



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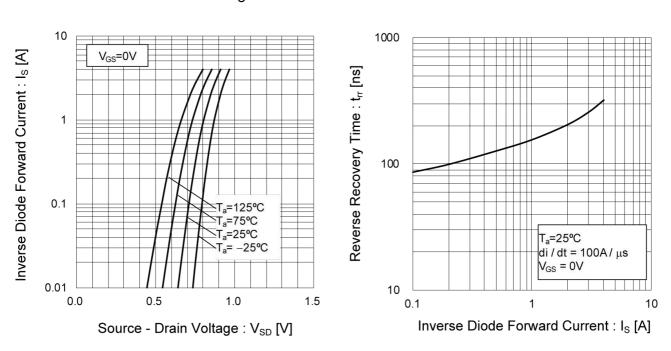
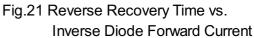


Fig.20 Inverse Diode Forward Current vs. Source - Drain Voltage





#### Measurement circuits

#### Fig.1-1 Switching Time Measurement Circuit

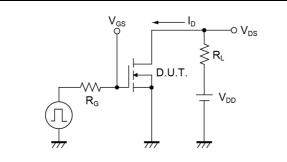


Fig.2-1 Gate Charge Measurement Circuit

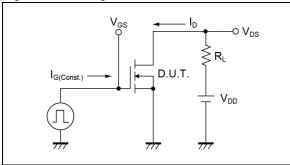


Fig.3-1 Avalanche Measurement Circuit

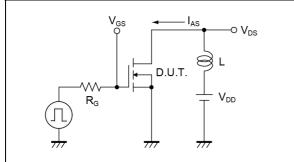


Fig.4-1 dv/dt Measurement Circuit

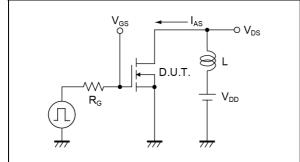
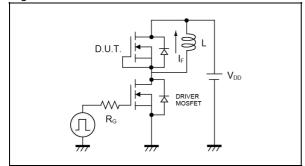
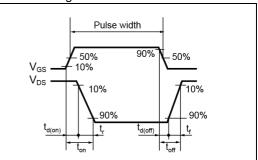


Fig.5-1 dv/dt Measurement Circuit

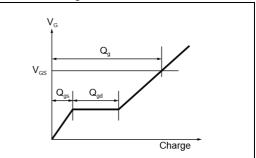


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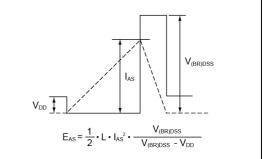
#### Fig.1-2 Switching Waveforms



#### Fig.2-2 Gate Charge Waveform



#### Fig.3-2 Avalanche Waveform



#### Fig.4-2 dv/dt Waveform

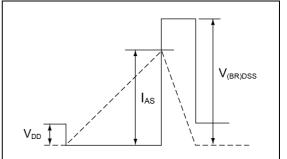
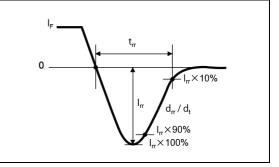
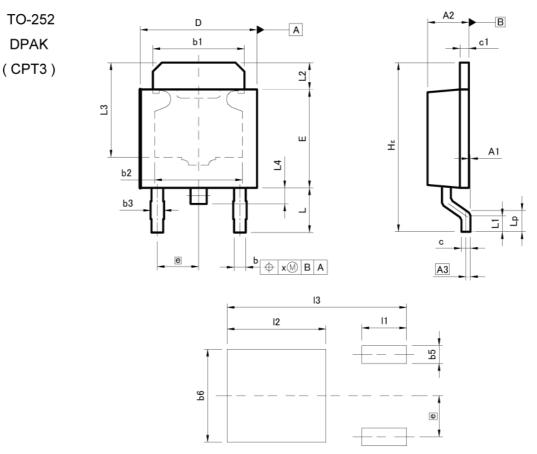


Fig.5-2 dv/dt Waveform





## Dimensions



Pattern of terminal position areas [Not a pattern of soldering pads]

DIM	MILIM	ETERS	INC	HES
	MIN	MAX	MIN	MAX
A1	0.00	0.15	0.000	0.006
A2	2.20	2.50	0.087	0.098
A3	0.	25	0.0	10
b	0.55	0.75	0.022	0.030
b1	5.00	5.30	0.197	0.209
b2	4.	90	0.1	93
b3		75	0.0	
C	0.40	0.60	0.016	0.024
c1	0.40	0.60	0.016	0.024
D	6.30	6.70	0.248	0.264
E	5.40	5.80	0.213	0.228
е	2.	2.30		91
HE	9.00	10.00	0.354	0.394
L	2.20	2.80	0.087	0.110
L1	0.80	1.40	0.031	0.055
L2	1.20	1.80	0.047	0.071
L3	5.30		0.2	09
L4	0.	90	0.0	35
Lp	1.00	1.60	0.039	0.063
x		0.25	<u>1</u> 27	0.010
	MILIM	ETERS	INC	HES
DIM -	MIN	MAX	MIN	MAX
b5		1.00	_	0.04
b6	-	5.20		0.205
11	1774	2.50		0.098
12	State of	5.50		0.217
13		10.00	<u></u>	0.394

Dimension in mm/inches



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(Note1) Medical Equipment Classification of the Specific	Applications
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JAPAN	USA	EU	CHINA
CLASSⅢ	CLASSI	CLASS II b	CLASSII
CLASSⅣ		CLASSⅢ	

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  - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
  - [f] Sealing or coating our Products with resin or other coating materials
  - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
  - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

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- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

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- 1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

#### Precaution for Storage / Transportation

- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
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  - [b] the temperature or humidity exceeds those recommended by ROHM
  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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